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Published before December 2004

Terms used:

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### 1 Robust mesh watermarking

Emil Praun, Hugues Hoppe, Adam Finkelstein

July 1999 SIGGRAPH '99: Proceedings of the 26th annual conference on Computer graphics and interactive techniques Publisher: ACM Press/Addison-Wesley Publishing Co.

Full text available: pdf(2.08 MB) Additional Information: [full citation](#), [references](#), [cited by](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 17, Downloads (12 Months): 112, Citation Count: 15

Keywords: copyright protection, steganography

### 2 Exploiting self-similarities to defeat digital watermarking systems: a case study on still images

Gwénaél Doërr, Jean-Luc Dugelay, Lucas Grangé

September 2004 MM&Sec '04: Proceedings of the 2004 workshop on Multimedia and security Publisher: ACM

Full text available: pdf(1.27 MB) Additional Information: [full citation](#), [abstract](#),

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 141, Citation Count: 0

Unauthorized digital copying is a major concern for multi-media content providers. Since copyright or unscrambled, digital watermarking has been introduced as a complementary ...

Keywords: block replacement attack, intra-signal collusion, self-similarities

### 3 Reconfigurable hardware solutions for the digital rights management of digital cinema

G. Rouvroy, F.-X. Standaert, F. Lefèuvre, J.-J. Quisquater, B. Macq, J.-D. Legat

October 2004 DRM '04: Proceedings of the 4th ACM workshop on Digital rights management Publisher: ACM

Full text available: pdf(440.86 KB) Additional Information: [full citation](#), [abstract](#),

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 106, Citation Count: 0

This paper presents a hardware implementation of a decoder for Digital Cinema images. This decoder uses 36 bits per pixels. It is the first implementation known nowadays that perfectly ...

Keywords: AES, DRM, FPGA, JPEG 2000, digital cinema, watermarking

4 Single-Chip MPEG-2 422P@HL CODEC LSI with Multi-Chip Configuration for Large Scale Hiroe Iwasaki, Jiro Naganuma, Koyo Nitta, Ken Nakamura, Takeshi Yoshitome, Mitsuo Ogura, Yasu March 2003 DATE '03: Proceedings of the conference on Design, Automation and Te Publisher: IEEE Computer Society

Full text available:  pdf(362.07 KB)  Publisher Site

Additional Information: full citation, abstract,

Bibliometrics: Downloads (6 Weeks): 13, Downloads (12 Months): 54, Citation Count: 1

This paper proposes a new architecture for VASA, a single-chip MPEG-2 422P@HL CODEC LSI w and demonstrates its flexibility and usefulness. This architecture consists of triple ...

5 Understanding the connectivity of heap objects

 Martin Hirzel, Johannes Henkel, Amer Diwan, Michael Hind

February 2003 ACM SIGPLAN Notices, Volume 38 Issue 2 supplement

Publisher: ACM

Full text available:  pdf(256.15 KB)

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Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 49, Citation Count: 10

Modern garbage collectors partition the set of heap objects to achieve the best performance. Fo their efforts on the youngest objects. Partitioning by age works well for many ...

Keywords: connectivity based garbage collection, object lifetimes

6 Understanding the connectivity of heap objects

 Martin Hirzel, Johannes Henkel, Amer Diwan, Michael Hind

June 2002 ISMM '02: Proceedings of the 3rd international symposium on Memory manage

Publisher: ACM

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